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UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2829 Examiner: J. Hollington

Applicant:	Mikio OHTAKI	
Appln. No.:	09/904,663	A NAVENION ATENIO
Filed:	July 16, 2001	<u>AMENDMENT</u>
For:	METHOD FOR MANUFACTURING AND BATCH TESTING SEMICONDUCTOR DEVICES)	
Atty Ref.:	KAN 120D1	July 13, 2004

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In Re PATENT APPLICATION Of:

Sir:

This Amendment is filed in response to the Office Action dated April 13, 2004, the time for response to which is up to and including July 13, 2004. Please amend the above-referenced application as shown on the following pages.

Certification Under 37 C.F.R. §1.8 (if applicable)

I hereby certify that this Amendment is being deposited with the United States Postal Service as First Class Mail under 37 C.F.R. §1.8 on this <u>July 13, 2004</u> and addressed to the Commissioner of Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Phillip G. Avruch

Typed or printed name of person mailing the Amendment

Signature of person mailing the Amendment